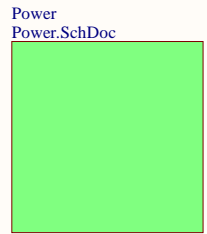


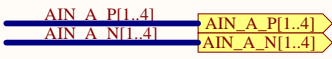
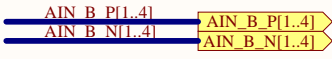
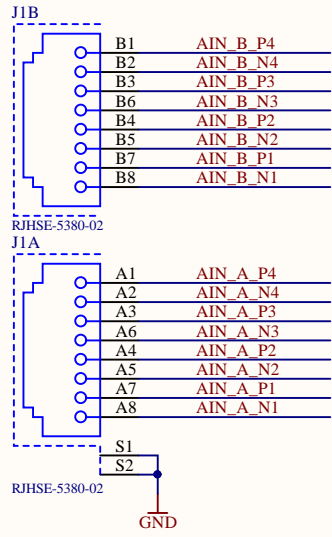
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OUT_N1	ADC_IN_A_N1
OUT_P2	ADC_IN_A_P2
OUT_N2	ADC_IN_A_N2
OUT_P3	ADC_IN_A_P3
OUT_N3	ADC_IN_A_N3
OUT_P4	ADC_IN_A_P4
OUT_N4	ADC_IN_A_N4

OUT_P5	ADC_IN_B_P1
OUT_N5	ADC_IN_B_N1
OUT_P6	ADC_IN_B_P2
OUT_N6	ADC_IN_B_N2
OUT_P7	ADC_IN_B_P3
OUT_N7	ADC_IN_B_N3
OUT_P8	ADC_IN_B_P4
OUT_N8	ADC_IN_B_N4

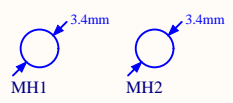
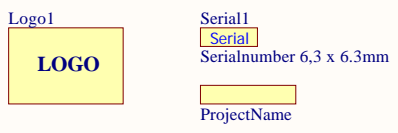
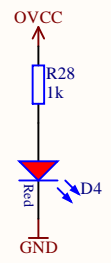
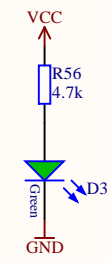
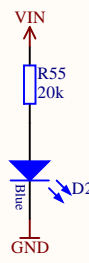
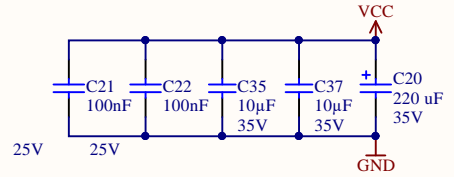
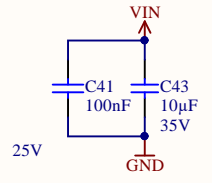
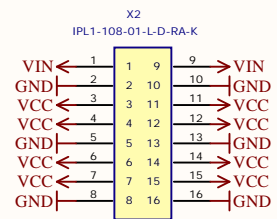


Title TopSheet.SchDoc		EAL TU München Arcisstraße 21 80333 München	
Size: A4	Revision: 2v03	Sheet 1 of 14	Author: Thomas Kreppel
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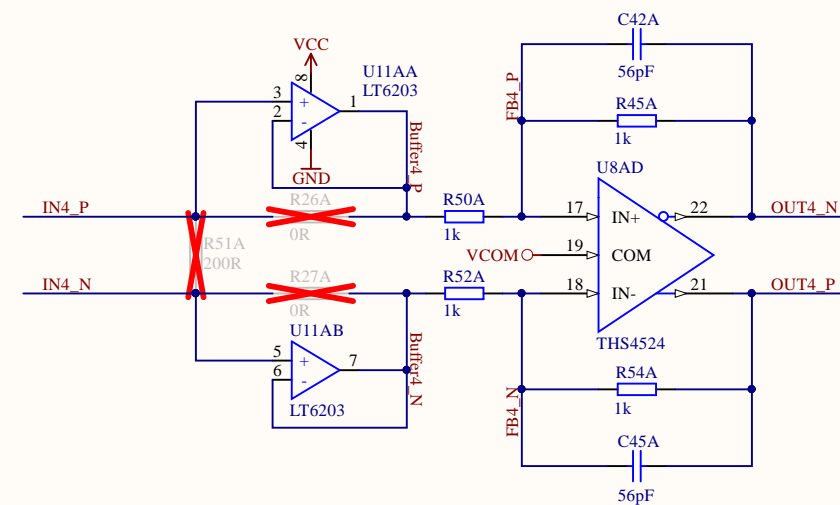
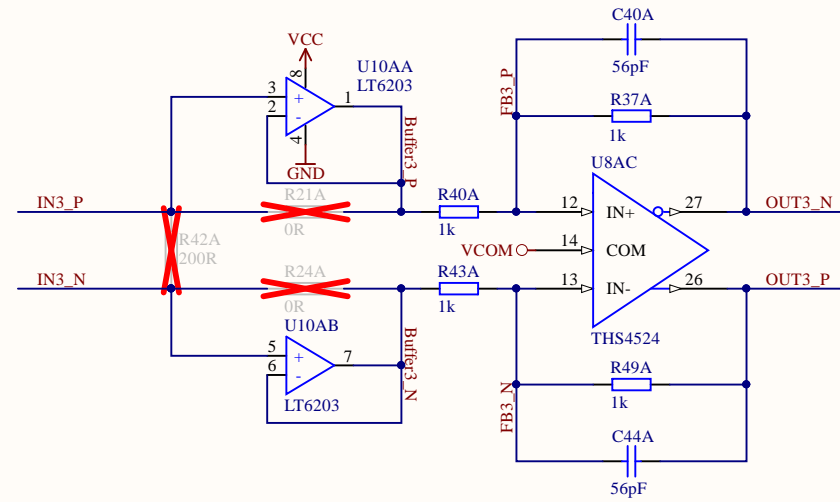
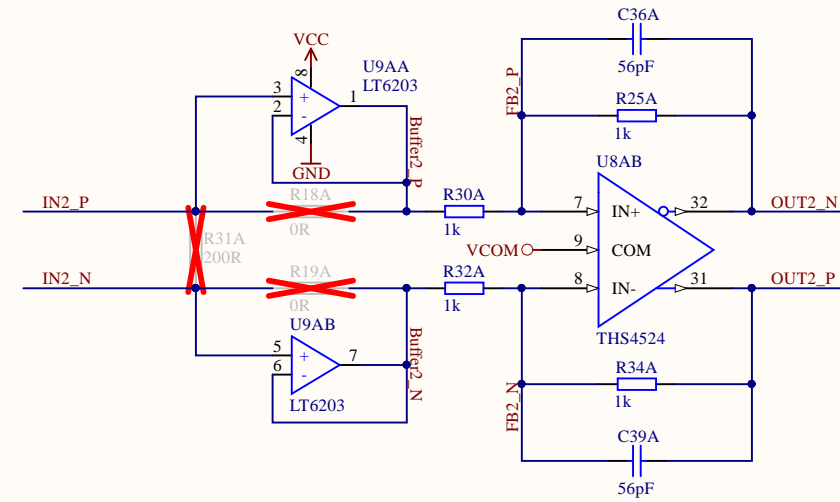
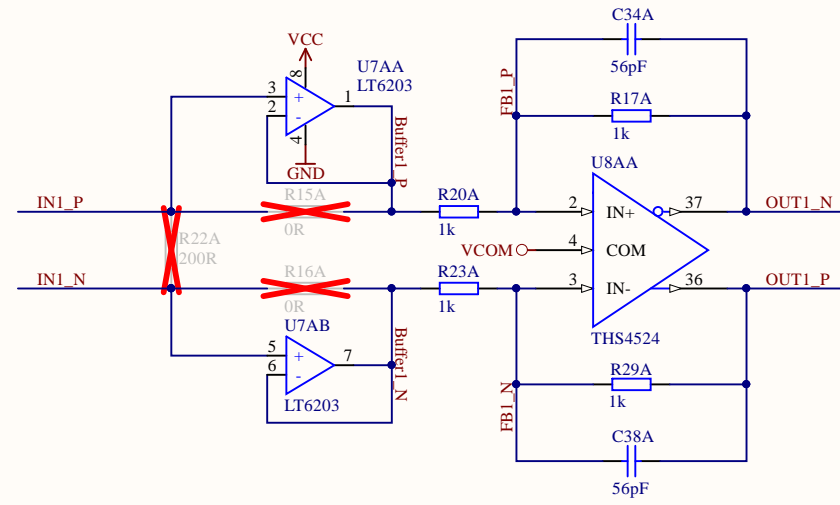


When using the VIN on the power connector X1, use pin 2 and 10 as ground connection

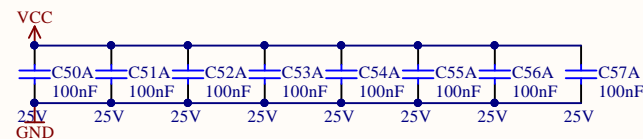
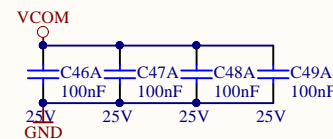
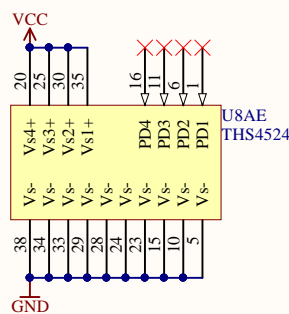


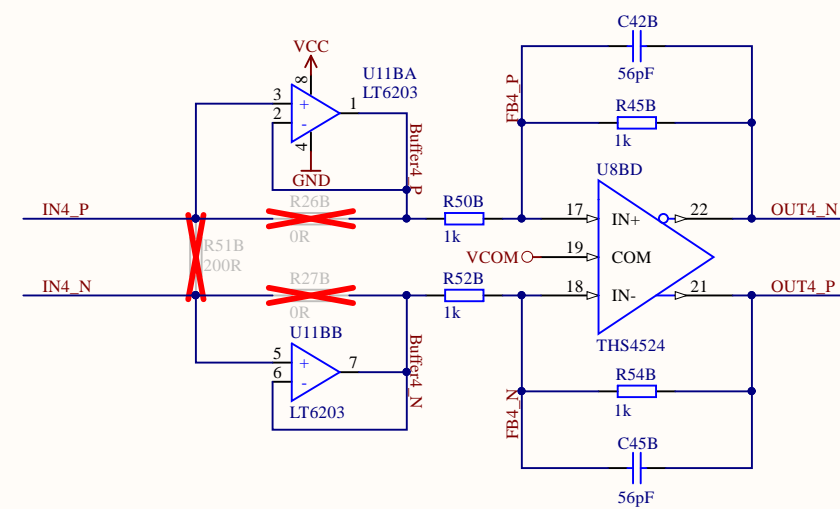
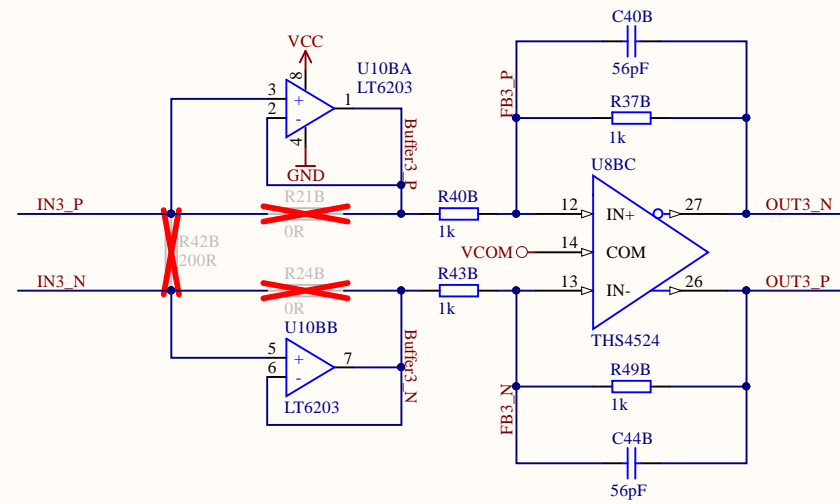
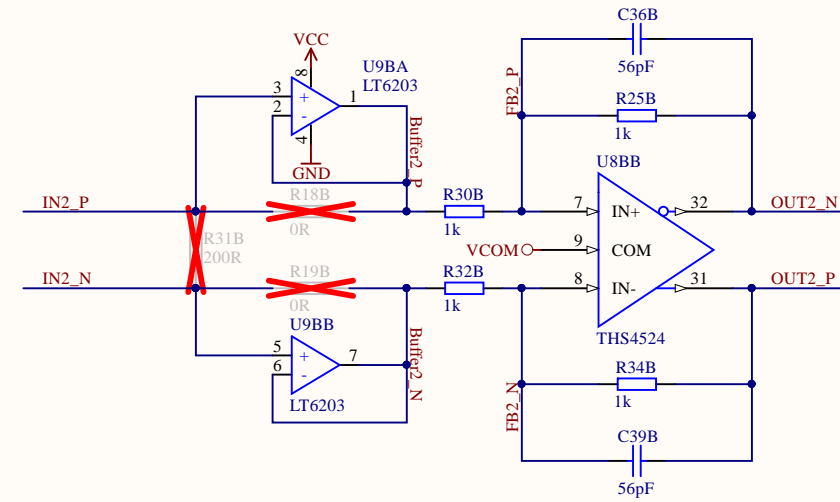
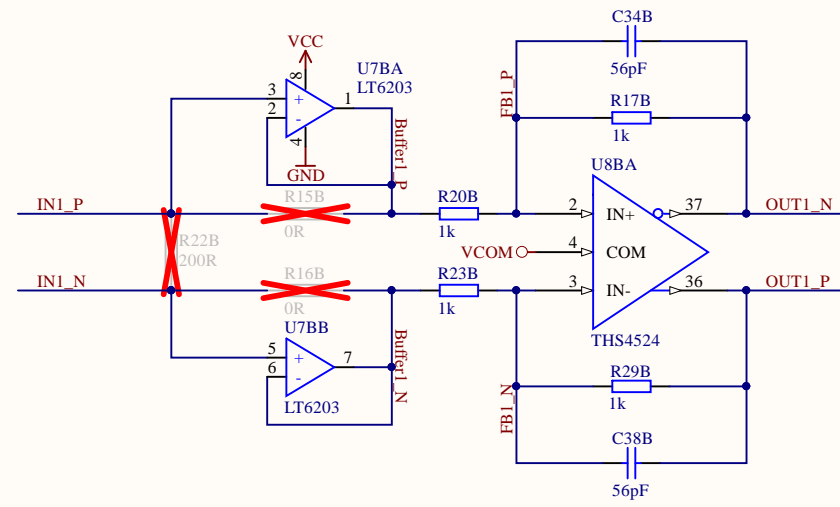
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Size: A4	Revision: 2v03	Sheet 2 of 14	Author: Thomas Kreppel
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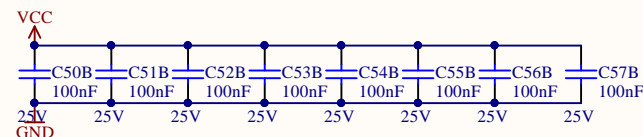
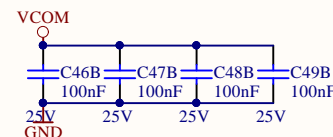
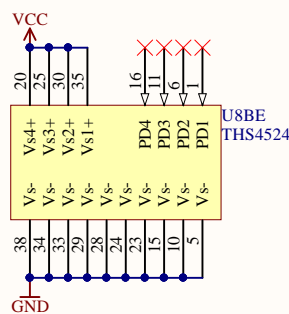


IN P[1..4]	IN P[1..4]
IN N[1..4]	IN N[1..4]
OUT P[1..4]	OUT P[1..4]
OUT N[1..4]	OUT N[1..4]
IN1_P	IN_P1
IN1_N	IN_N1
IN2_P	IN_P2
IN2_N	IN_N2
IN3_P	IN_P3
IN3_N	IN_N3
IN4_P	IN_P4
IN4_N	IN_N4
OUT1_P	OUT_P1
OUT1_N	OUT_N1
OUT2_P	OUT_P2
OUT2_N	OUT_N2
OUT3_P	OUT_P3
OUT3_N	OUT_N3
OUT4_P	OUT_P4
OUT4_N	OUT_N4
FB1_P	
FB1_N	
FB2_P	
FB2_N	
FB3_P	
FB3_N	
FB4_P	
FB4_N	
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Buffer2_N	
Buffer3_N	
Buffer4_N	



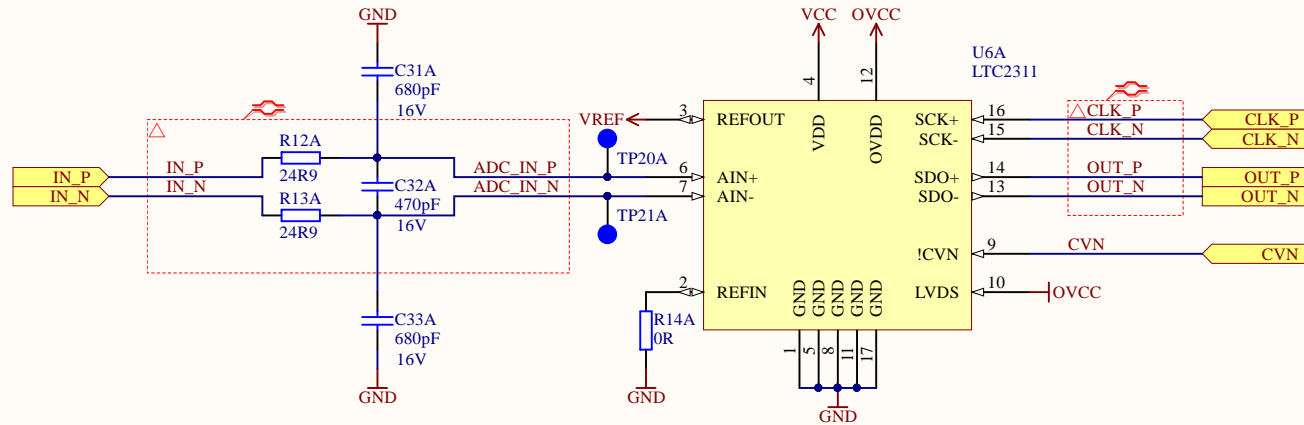
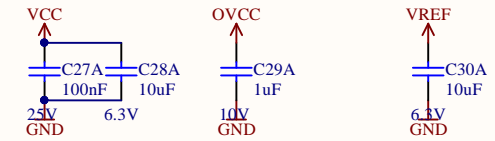


IN P[1..4]	IN P[1..4]
IN N[1..4]	IN N[1..4]
OUT P[1..4]	OUT P[1..4]
OUT N[1..4]	OUT N[1..4]
IN1_P	IN_P1
IN1_N	IN_N1
IN2_P	IN_P2
IN2_N	IN_N2
IN3_P	IN_P3
IN3_N	IN_N3
IN4_P	IN_P4
IN4_N	IN_N4
OUT1_P	OUT_P1
OUT1_N	OUT_N1
OUT2_P	OUT_P2
OUT2_N	OUT_N2
OUT3_P	OUT_P3
OUT3_N	OUT_N3
OUT4_P	OUT_P4
OUT4_N	OUT_N4
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FB1_N	
FB2_P	
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FB3_N	
FB4_P	
FB4_N	
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Buffer3_N	
Buffer4_N	





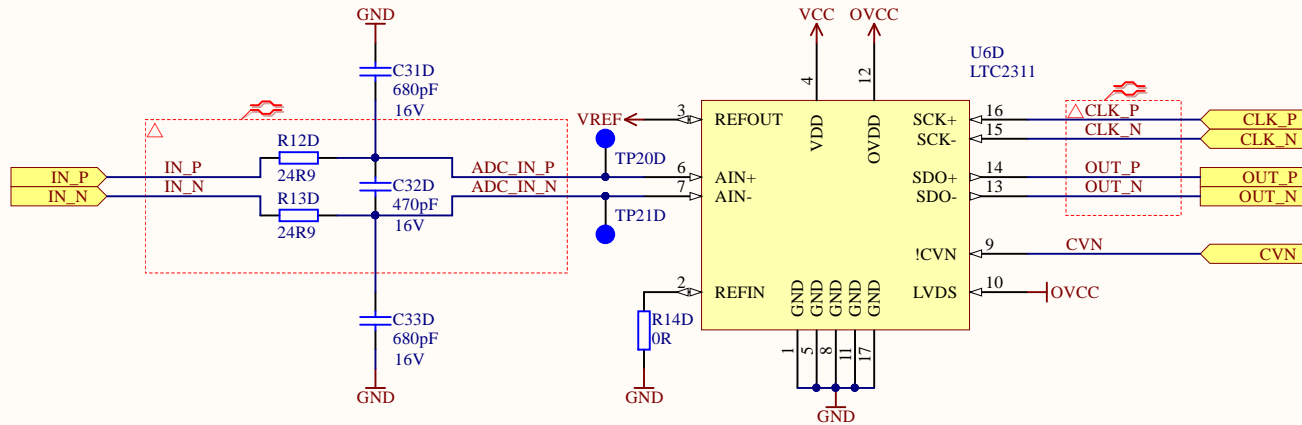
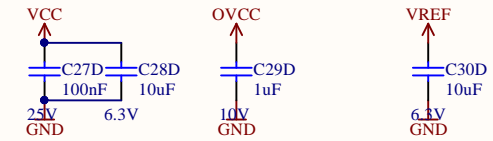
ADC can provide internal 4.096V reference. For that purpose R47 has to be replaced by 10u capacitor



Title ADC.SchDoc		EAL TU München Arcisstraße 21 80333 München	
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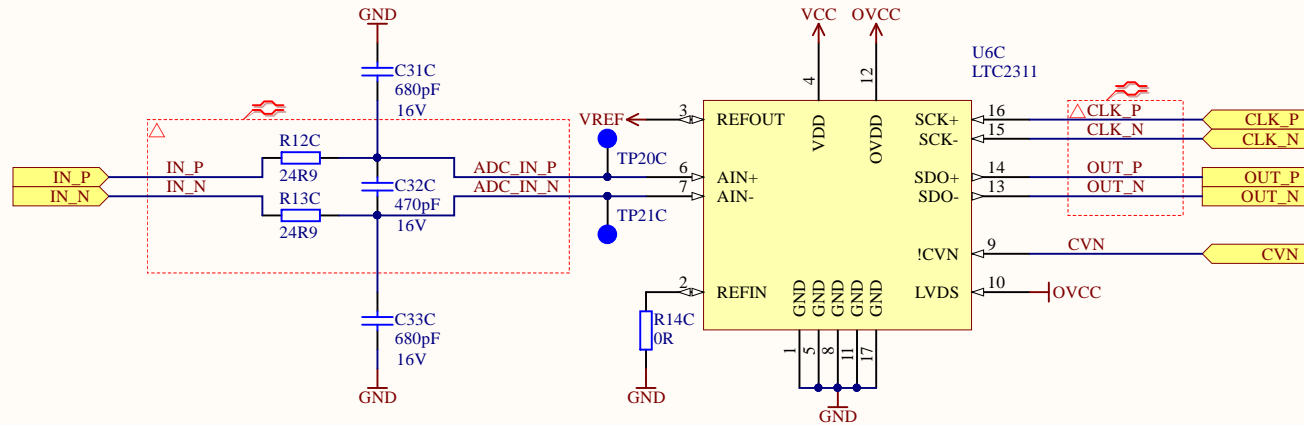
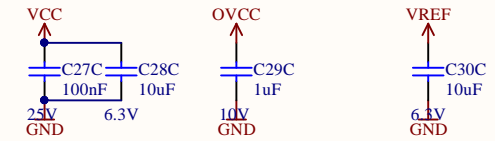
ADC can provide internal 4.096V reference. For that purpose R47 has to be replaced by 10u capacitor



Title ADC.SchDoc		EAL TU München Arcisstraße 21 80333 München	
Size: A4	Revision: 2v03		
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Project: UltraZohm_ADC.PrjPCB			



ADC can provide internal 4.096V reference. For that purpose R47 has to be replaced by 10u capacitor

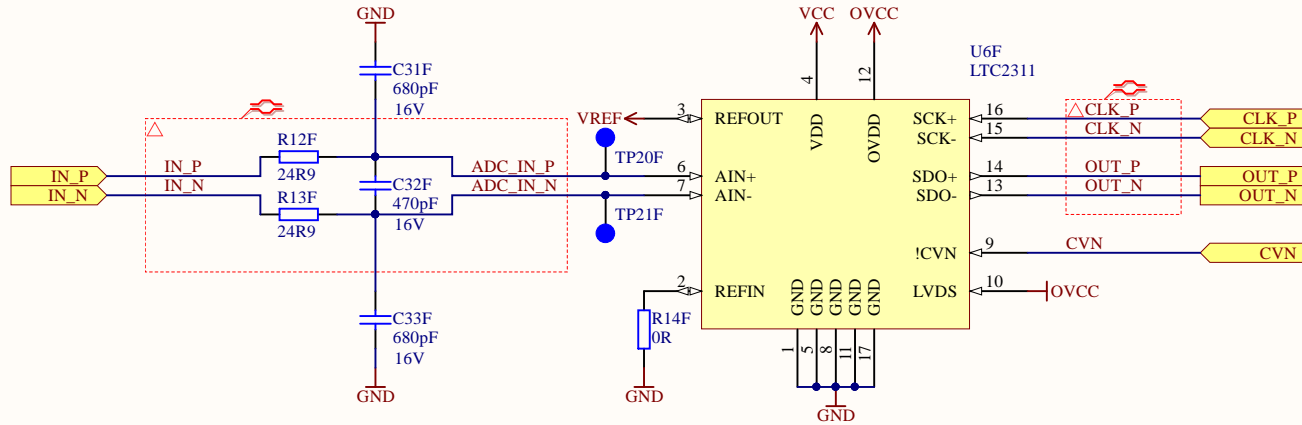
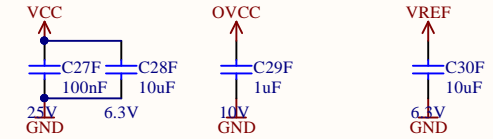


Title ADC.SchDoc		EAL TU München Arcisstraße 21 80333 München	
Size: A4	Revision: 2v03		
Date: 17.09.2019	Time: 18:33:56	Sheet 4.2 of 14	Author: Thomas Kreppel
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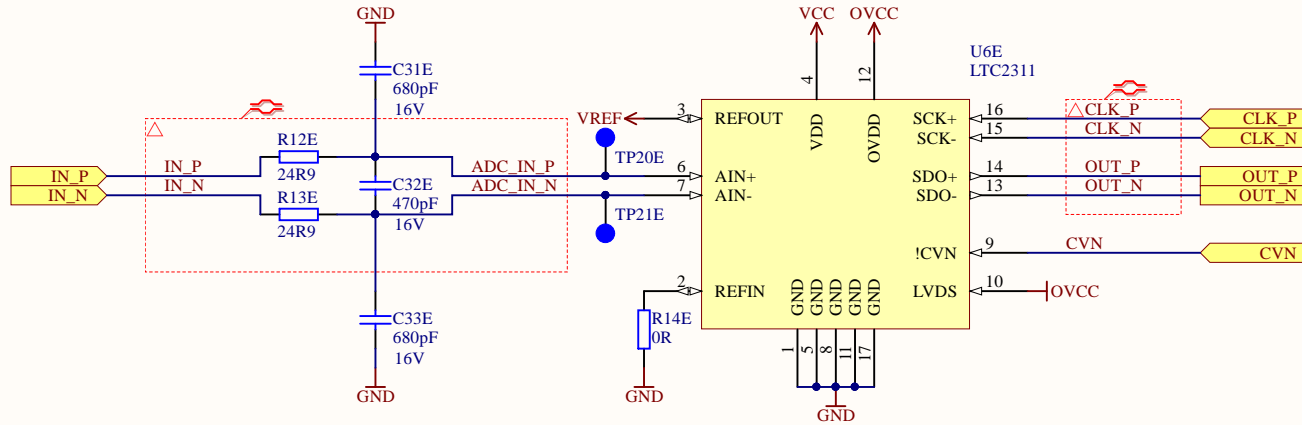
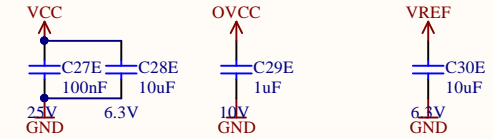
ADC can provide internal 4.096V reference. For that purpose R47 has to be replaced by 10u capacitor



Title ADC.SchDoc		EAL TU München Arcisstraße 21 80333 München	
Size: A4	Revision: 2v03		
Date: 17.09.2019	Time: 18:33:56	Sheet 4.3 of 14	Author: Thomas Kreppel
Project: UltraZohm_ADC.PrjPCB			



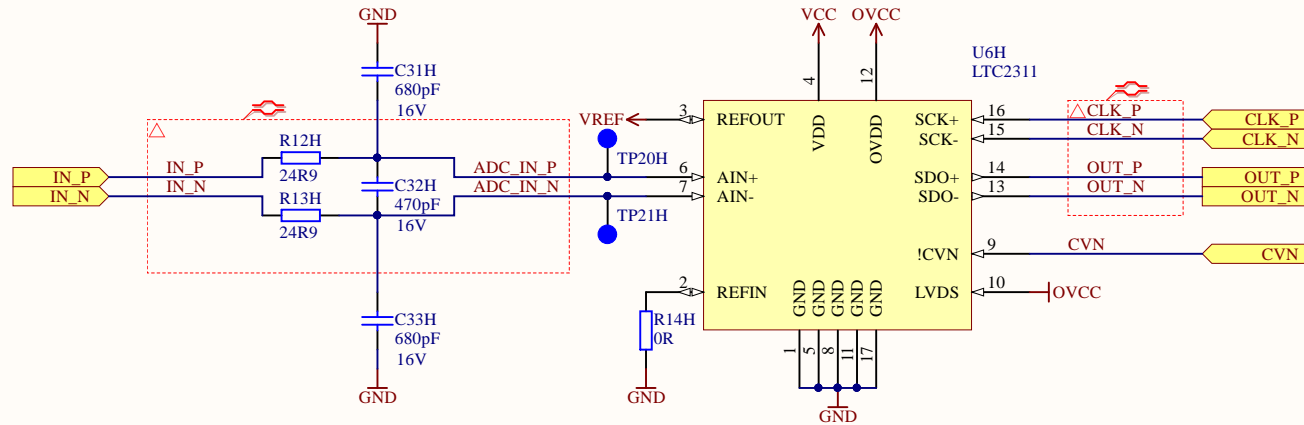
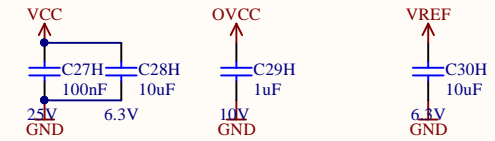
ADC can provide internal 4.096V reference. For that purpose R47 has to be replaced by 10u capacitor



Title ADC.SchDoc		EAL TU München Arcisstraße 21 80333 München	
Size: A4	Revision: 2v03		
Date: 17.09.2019	Time: 18:33:56	Sheet 4.3 of 14	Author: Thomas Kreppel
Project: UltraZohm_ADC.PrjPCB			



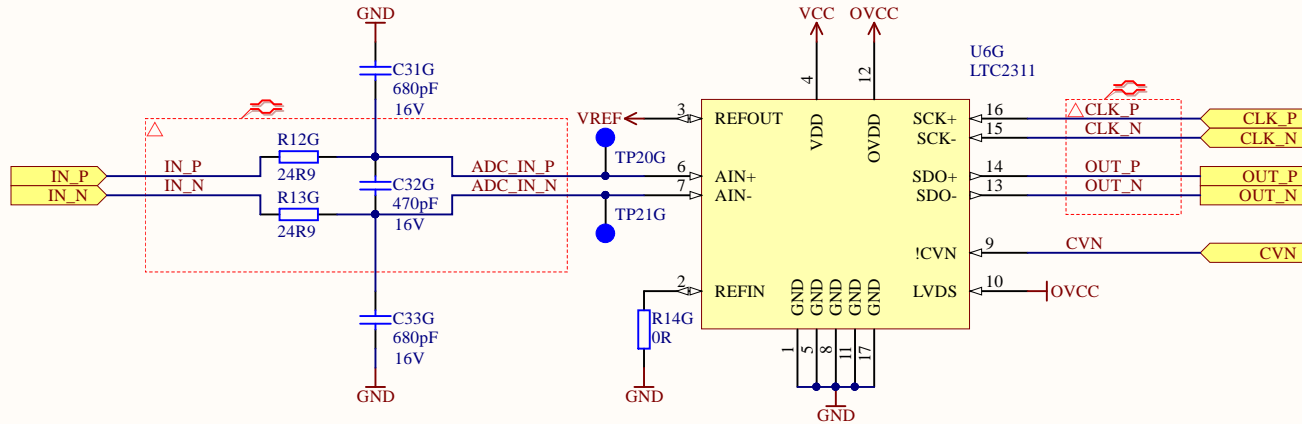
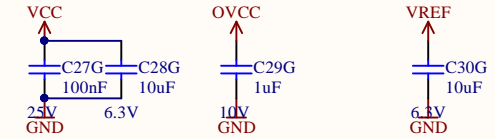
ADC can provide internal 4.096V reference. For that purpose R47 has to be replaced by 10u capacitor



Title ADC.SchDoc		EAL TU München Arcisstraße 21 80333 München	
Size: A4	Revision: 2v03		
Date: 17.09.2019	Time: 18:33:56	Sheet 4.4 of 14	Author: Thomas Kreppel
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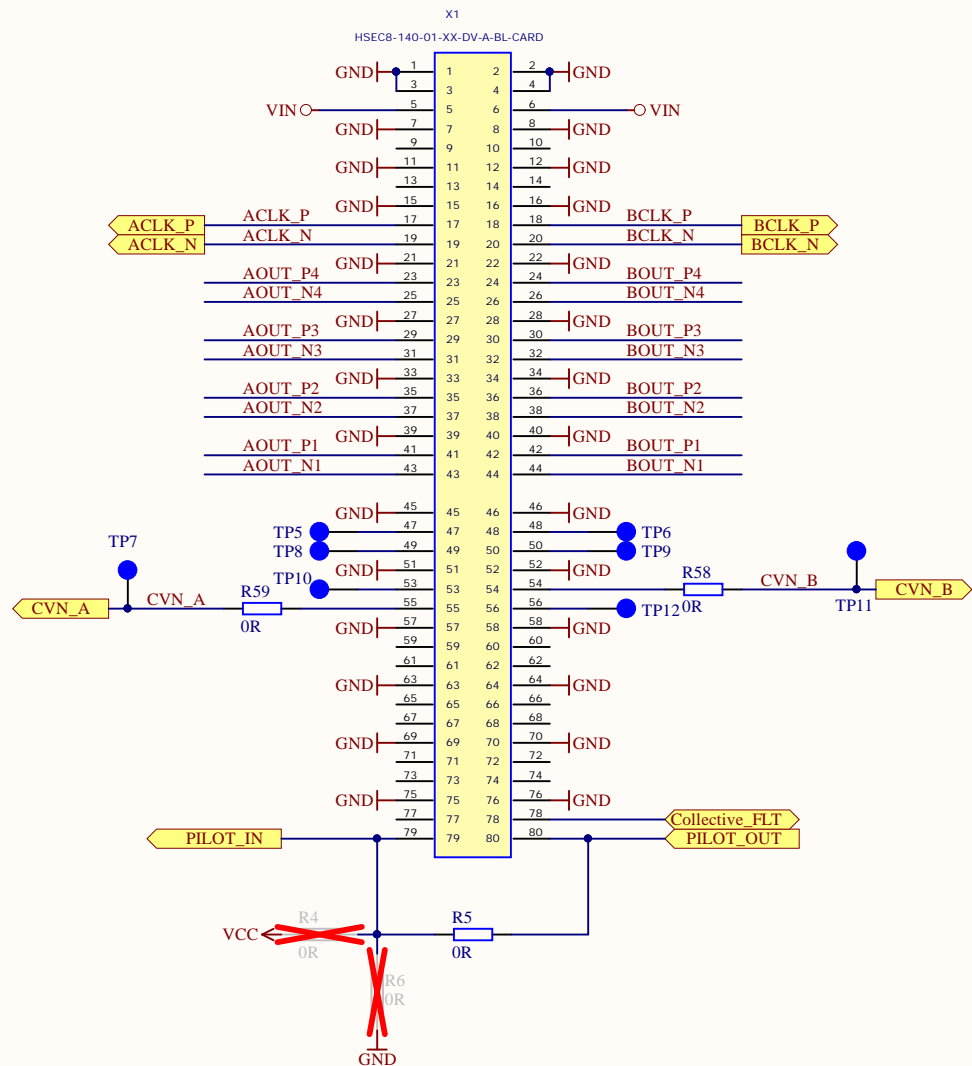
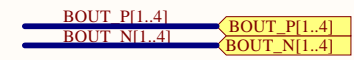
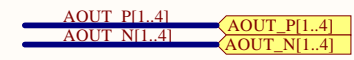
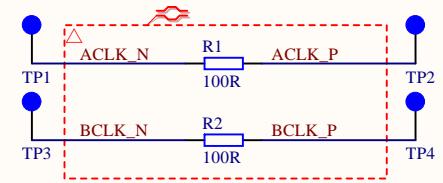
ADC can provide internal 4.096V reference. For that purpose R47 has to be replaced by 10u capacitor



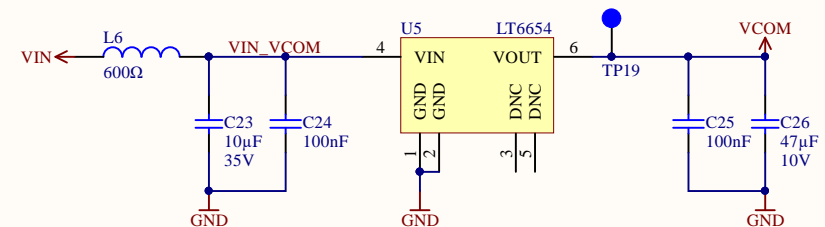
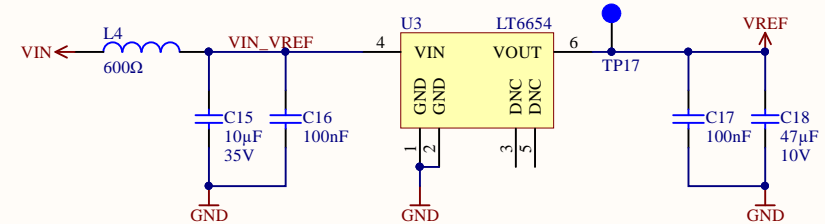
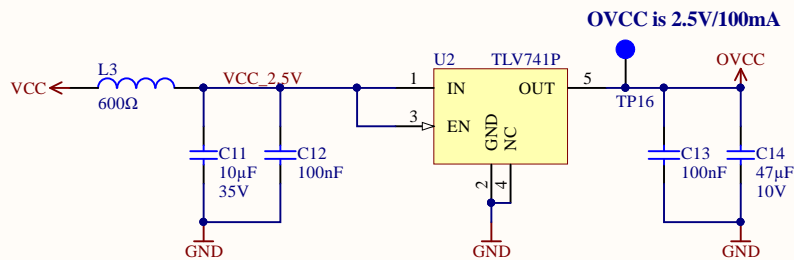
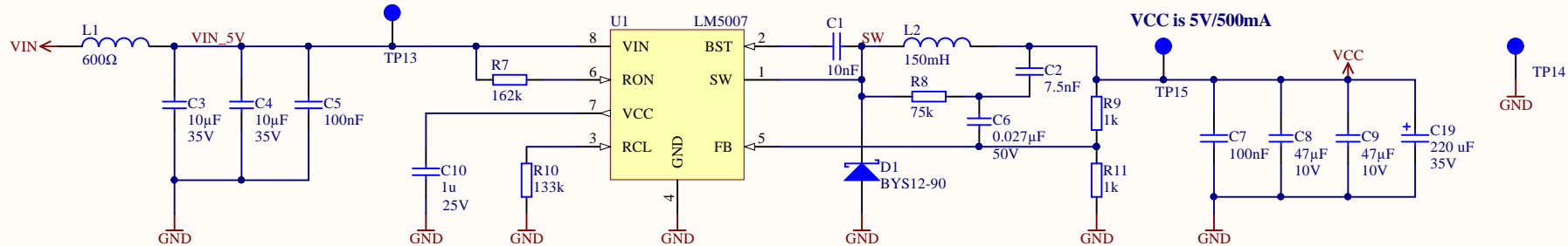
Title ADC.SchDoc		EAL TU München Arcisstraße 21 80333 München	
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If R60 is fitted with 0R all ADCs are controlled with one conversion signal. R61 and R62 are then dnp  
 R50 and R51 (R61 and R62) are used to adjust 3.3V CVN signal to 2.5V level  
 R52, R53 and R54 are fitted appropriately for Pilot In/Out



Title CarrierConnector.SchDoc		EAL TU München Arcisstraße 21 80333 München		
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LT6655 is available in 1.25V/2.048V/2.5V/3V/3.3V/4.096V/5V  
 If the internal reference from LTC2311 is used U9 must not be fitted  
 U10 is only required for single ended signals with offsetpotential

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Project: UltraZohm_ADC.PrjPCB			

